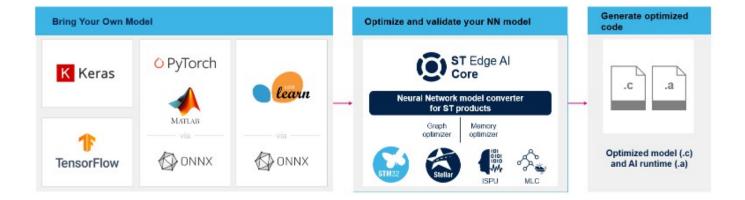


Data brief

Artificial intelligence (AI) optimizer technology for STMicroelectronics products





Product status link STEdgeAl-Core

Features

- Generation of an optimized library from pre-trained neural network and classical machine learning models for supported ST products
- Support of STM32 microcontrollers and microprocessors, Stellar microcontrollers, and smart sensors with ISPU and MLC
- Support for various deep learning frameworks such as Keras and TensorFlow™ lite
- Support for all frameworks that can export to the ONNX standard format such as PyTorch™, MATLAB®
- Support for various built-in scikit-learn models such as isolation forest, support vector machine (SVM), K-means via ONNX
- Provide detailed information about AI model RAM and flash memory sizes
- Several optimization options are available (time, size or balanced)
- Validate optimized model against reference model on host and on target
- Support for 32-bits float and 8-bit quantized neural network formats (TensorFlow™ lite and ONNX tensor-oriented QDQ)
- Support for deeply quantized neural networks (down to 1-bit) from QKeras and Larq
- Desktop tool available for Windows, MacOS, Linux
- Free-of-charge, user-friendly license terms

Description

STEdgeAl-Core is a free-of-charge desktop tool to evaluate, optimize and compile edge Al models for multiple ST products, including microcontrollers, microprocessors, and smart sensors with ISPU and MLC.

It is available as a command-line interface (CLI) and it allows an automatic conversion of pretrained artificial intelligence algorithms. Including neural network and classical machine learning models, into the equivalent optimized C code to be embedded in the application.

The generated optimized library offers an easy-to-use and developer-friendly way to deploy AI on edge devices.



The tool offers several means to benchmark and validate artificial intelligence algorithms both on a personal workstation (Windows, Linux, Mac) or directly on the target ST platform.

The user manual is integrated into the tool itself in a convenient HTML format.

The STEdgeAl-Core technology is part of the ST Edge Al Suite, which is an integrated collection of software tools, designed to facilitate the development and deployment of embedded Al applications.

This comprehensive suite supports both optimization and deployment of machine learning algorithms.

The suite aids in managing neural network models, easing the process from data gathering to hardware deployment for users in different fields.

The ST Edge Al Suite supports various ST products: STM32 microcontrollers and microprocessors, Stellar microcontrollers, and smart sensors.

The ST Edge Al Suite is a strategic tool democratizing edge Al for developers, enabling efficient, effective Al deployment in embedded systems.

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Revision history

Table 1. Document revision history

Date	Revision	Changes
13-Jun-2024	1	Initial release.

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