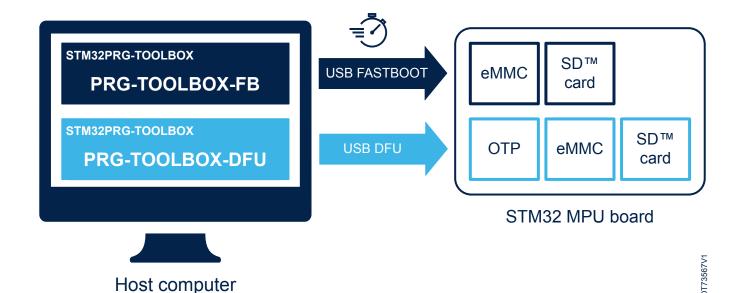
Data brief

## Programming toolbox in source code for STM32 microprocessors



Product status link
STM32PRG-TOOLBOX

### **Features**

#### **PRG-TOOLBOX-DFU**

- U-Boot installation with or without launching fastboot mode
- OTP fuse: read and write operations
- OTP management with the STM32PRGFW-UTIL firmware tool
- Supported target memory: SD<sup>™</sup> card, eMMC
- Multi-OS support: Windows<sup>®</sup> and Linux<sup>®</sup>

#### PRG-TOOLBOX-FB

- Memory partition programming over the fastboot interface
- Programming performance boost (versus DFU mode)
- Supported target memory: SD<sup>™</sup> card, eMMC
- Multi-OS support: Windows<sup>®</sup> and Linux<sup>®</sup>



### **Description**

STM32PRG-TOOLBOX provides an open-source multi-OS software package to program and update the target flash memory of STM32 microprocessors through the USB fastboot interface.

STM32PRG-TOOLBOX is composed of two separate tools:

- PRG-TOOLBOX-DFU to install U-Boot, launch the fastboot mode, and manage OTP
- PRG-TOOLBOX-FB to program memory partitions through the fastboot interface, and boost programming performance

Users can start from these preconfigured projects to configure their STM32 microprocessor devices and customize the boot/programming sequences according to their specific use cases. For more information about each project, see STM32PRG-TOOLBOX in the STM32 MPU wiki at wiki.st.com/stm32mpu and refer to:

- PRG-TOOLBOX-DFU release note
- PRG-TOOLBOX-FB release note

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# 1 General information

### 1.1 Ordering information

The STM32PRG-TOOLBOX projects are available for free download from GitHub at github.com/STMicroelectronics:

- PRG-TOOLBOX-DFU: github.com/STMicroelectronics/STM32PRG-TOOLBOX-DFU
- PRG-TOOLBOX-FB: github.com/STMicroelectronics/STM32PRG-TOOLBOX-FB

### 1.2 License

STM32PRG-TOOLBOX is delivered as a full open-source package for use with STM32 microprocessor devices based on the Arm® Cortex®-A processor.

Refer to PRG-TOOLBOX-DFU and PRG-TOOLBOX-FB on GitHub for their corresponding licenses.

Note: Arm is a registered trademark of Arm Limited (or its subsidiaries) in the US and/or elsewhere.

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# **Revision history**

Table 1. Document revision history

Date	Revision	Changes
26-Apr-2024	1	Initial release.

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