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# STSW-ILL035GUI Quick Start Guide

STMicroelectronics

APMS - Analog sub group - GPA

# GUI Main Features

GUI Title and Board Status

Board Settings

Device Toolbar

Load/Save Configuration

Channels Configuration

Device Status

Device Settings

STEVAL-ILL035V1 Version 0.9 - Eval Board Detected

Load Save  LREN EXT CLK 5.3MHz  Enable  Reload  Enable CHs  Disable CHs

Channel	Value
16	443
15	517
14	443
13	1070
12	1513
11	2657
10	3579
9	2768
8	1550
7	701
6	1476
5	2509
4	3616
3	2878
2	2214
1	369

CHSEL

CHSTA

EDM OVFW OTAF CFP OVRS VB 32.3V

DEVCFG0	
Open Channel Auto Disc.	Disabled
Shorted Channel Auto Disc.	Disabled
Regulation Window	600mV
LED Short Detection	Disabled
Grayscale Mode	Grayscale Dimming
Current Range	20mA - 45mA
Output Voltage Regulation	Enabled
Slave Device Mode	No Slave Devices

DEVCFG1	
Current Gain Range	1 - 1.492
Current Gain Adjust	0
Dimming Latency Cycles Range	0 - 64K
Brightness Data Format	16x16 (12bit res)
Dimming Timing Inversion	Off Phase First
Brightness data update	On LE Falling Edge
Dimming Synch Pin	GSSY Output Pin
Continuous Status Reading	DISABLED

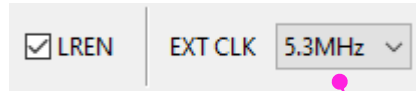
# GUI Title and Toolbar



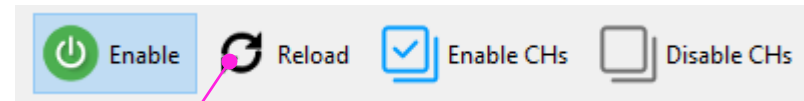
The GUI Title shows the SW version and informs about the connection status of the STEVAL board.



Using the Load/Save buttons, the user can save any device configuration in a .cfg file. Once loaded, the content of the .cfg file are applied immediately. The configuration files store the EXT CLK setting, but not the LREN setting.



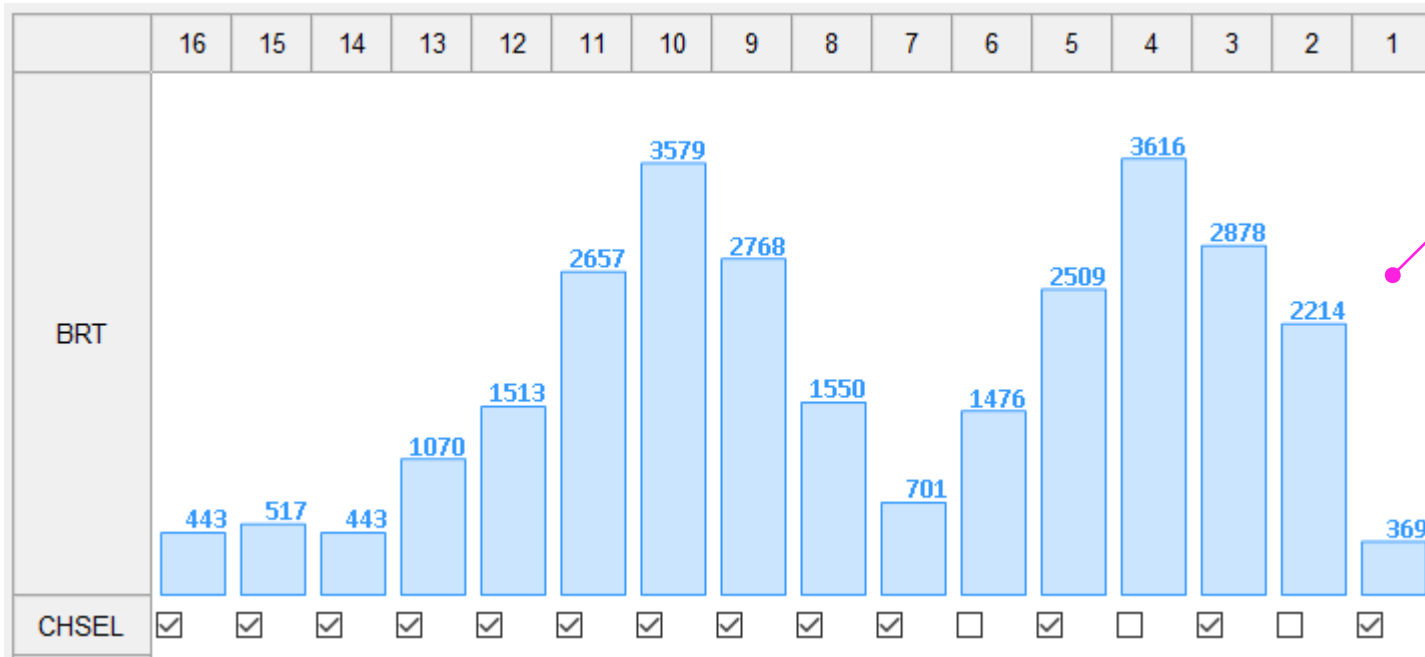
The board settings control the LREN and the GSCK pins of the LED7708. Once a STEVAL board is detected, the LREN setting is automatically enabled, in order to operate with the device.



## Device Toolbar

The **Enable** button acts on the DEN bit of the DEVCFG0 register  
The **Reload** button reads all the Devices registers refresh the content of the GUI.  
The **Enable/Disable CHs** buttons, enables or disables all the device channels.

# Channels Configuration and Status



From here, the user can enable and set the brightness of each individual channel. The number upon each channel bar indicates the register value of the channel brightness. The full scale of the number can change according to the **Brightness Data Format** setting located on the DEVCFG1 settings (right side of the GUI). To change the BRTx of any channel, just drag up or down the bar.



The channels status are reported here. Each indicator is the bit of the CHSTA register. Below are reported the DEVCFGx readonly bits. The status is refreshed periodically.

The device BOOST voltage is refreshed periodically and showed here.



# DEVCFGx and GSLAT

DEVCFG0	
Open Channel Auto Disc.	Disabled
Shorted Channel Auto Disc.	Disabled
Regulation Window	600mV
LED Short Detection	Disabled
Grayscale Mode	Grayscale Dimming
Current Range	20mA - 45mA
Output Voltage Regulation	Enabled
Slave Device Mode	No Slave Devices
DEVCFG1	
Current Gain Range	1 - 1.492
Current Gain Adjust	0
Dimming Latency Cycles Range	0 - 64K
Brightness Data Format	16x16 (12bit res)
Dimming Timing Inversion	Off Phase First
Brightness data update	On LE Falling Edge
Dimming Synch Pin	GSSY Output Pin
Continuous Status Reading	DISABLED
GSLAT	

The most of the DEVCFGx register fields, and the GSLAT as well, are reported here. Each time a value is updated, the SW writes the new configuration, and subsequently, reload the register involved to be sure the value has been applied.

Note: a particular attention must be taken when changing the **Output Voltage Regulation** field. If its value is **disabled**, the BOOST won't work and the LED strings will not be powered by the STEVAL.

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