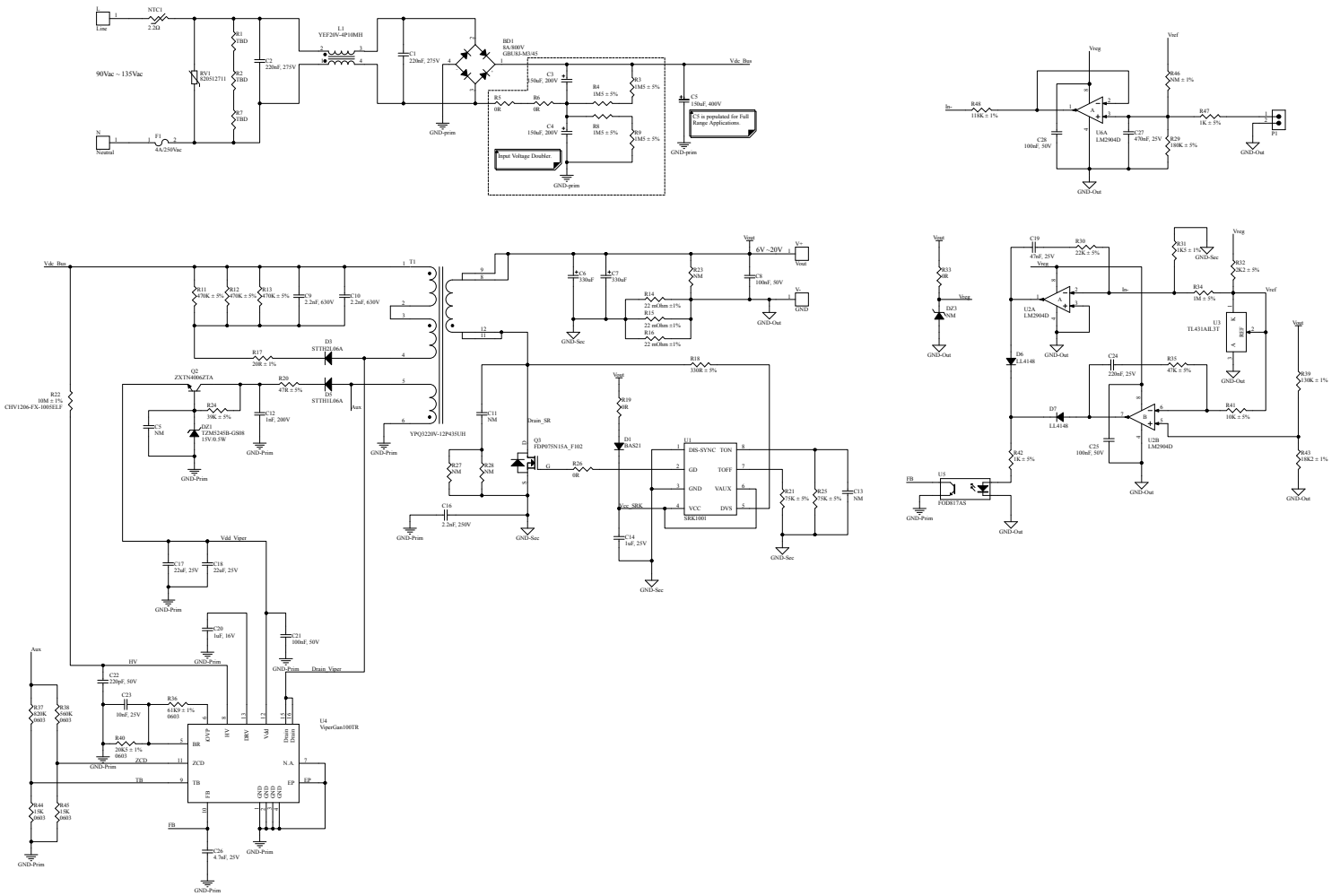


Schematic diagrams

Figure 1. STDES-VGAN100F1 circuit schematic



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