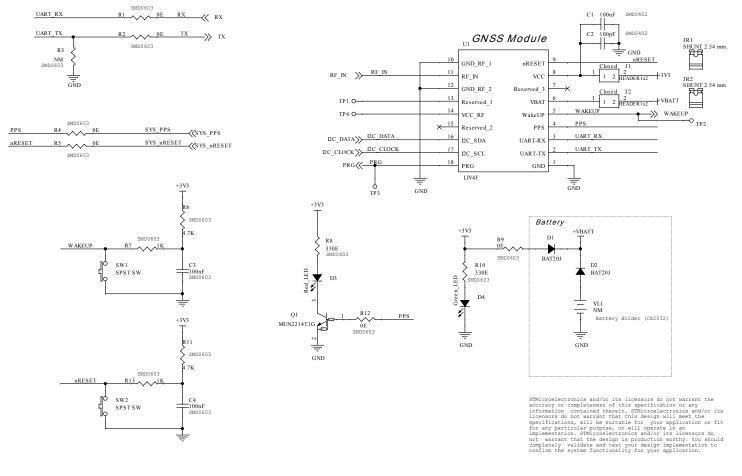
# X-NUCLEO-LIV4A1



## Schematic diagrams



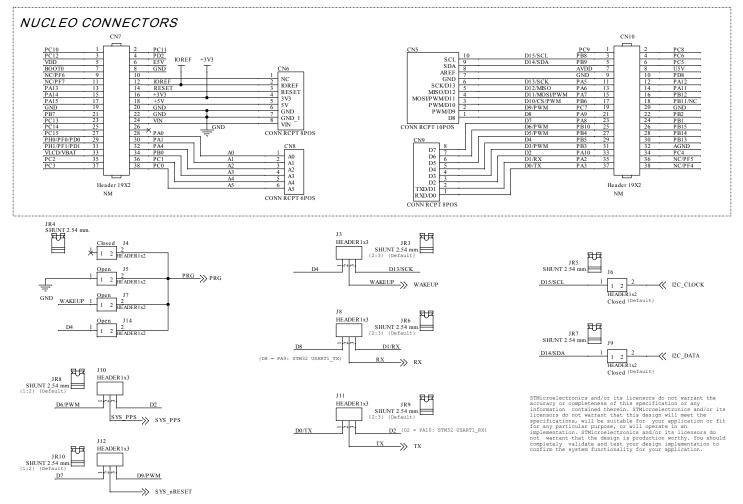
### Figure 1. X-NUCLEO-LIV4A1 circuit schematic (1 of 3)

X-NUCLEO-LIV4A1 - Rev 1 - January 2024 For further information contact your local STMicroelectronics sales offic



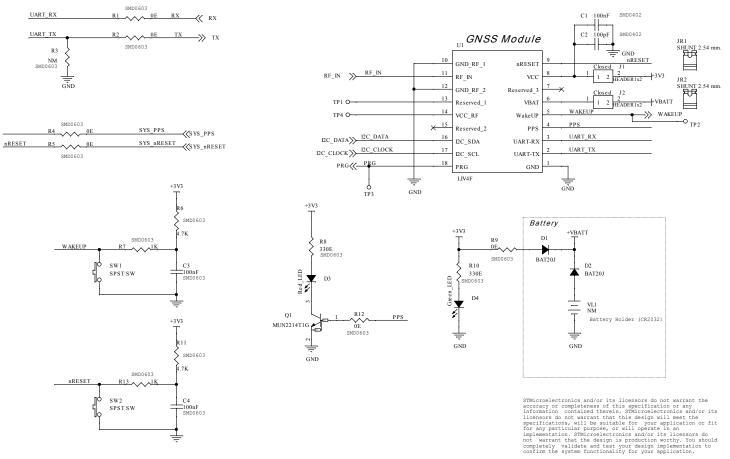






### X-NUCLEO-LIV4A1





#### Figure 3. X-NUCLEO-LIV4A1 circuit schematic (3 of 3)



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