

Security advisory TN1518-ST-PSIRT: STM32CubeWB secure memory access

Overview

This security advisory pertains to the SRAM2B memory configuration performed by the STM32CubeWB software, potentially allowing access to a part of the secure memory.

Affected products

Product	Version	Type	Note
STM32CubeWB	v1.15.0, v1.16.0, v1.17.0	embedded software	-

Description

Due to the memory configuration performed by the STM32CubeWB software versions listed in Affected products, the CPU 2 (secure, Cortex-M0) can overwrite a part of the CPU 1 (nonsecure) SRAM2B memory.

Impact

The issue potentially allows access to part of the content stored in the secure area managed by the CPU 2 (secure, Cortex-M0).

Remediation

The issue is fixed in STM32CubeWB starting from the version v1.17.1.

The updates can be found at the following location: <https://www.st.com/en/embedded-software/stm32cubewb.html>.

ST recommends that the users update their software to the most recent version.

Contact information

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Revision history

Table 1. Document revision history

Date	Version	Changes
22-May-2024	1	Initial release.

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