

Software expansion for STM32Cube driving industrial digital output based on IPS

Applications & demonstrations	Smart driving example
Hardware Abstraction	STM32Cube Hardware Abstraction Layer (HAL)
Hardware	<p>STM32 Nucleo expansion boards X-NUCLEO-OUT1A2, X-NUCLEO-OUT0A1, X-NUCLEO-OUT0A4, X-NUCLEO-OUT0A1, X-NUCLEO-OUT0A1 X-NUCLEO-OUT0A1, X-NUCLEO-OUT0A1, X-NUCLEO-OUT0A1, X-NUCLEO-OUT0A1, X-NUCLEO-OUT0A1 X-NUCLEO-OUT1A1, X-NUCLEO-OUT1A1, X-NUCLEO-OUT1A1, X-NUCLEO-OUT1A1, X-NUCLEO-OUT1A1 X-NUCLEO-OUT1A1, X-NUCLEO-OUT1A1, X-NUCLEO-OUT1A1, X-NUCLEO-OUT1A1, X-NUCLEO-OUT1A1 X-NUCLEO-OUT1A1</p> <p>STM32 Nucleo development board NUCLEO-F401RE/G431RB</p>



Features

- Software package to build applications using the expansion boards that mount the industrial intelligent power switches (IPS)
- Includes ready-to-use firmware to evaluate easily the driving and diagnostic capabilities of the IPS products for industrial loads
- Support for **NUCLEO-F401RE** and **NUCLEO-G431RB** development boards
- GPIOs, PWMs, and IRQs
- Fault/diagnostics interrupt handling
- Compatible with **STM32CubeMX**, can be downloaded from [st.com](https://www.st.com) and installed directly into **STM32CubeMX**
- Easy portability across different MCU families, thanks to **STM32Cube**
- Free, user-friendly license terms

Description

Product summary	
Software expansion for STM32Cube driving industrial digital output based on IPS	X-CUBE-IPS
Single channel high-side switches	IPS1025H/ IPS1025H-32/ IPS1025HF/ IPS160HF/ IPS161HF
Dual channel high-side switches	IPS2050H/ IPS2050H-32
Quad low-side intelligent power switch	IPS4260L
Octal channel galvanic isolated high-side switches	ISO808/ISO808-1/ ISO808A/ ISO808A-1/ ISO8200BQ
Octal channel high side switches	IPS8160HQ/ IPS8160HQ-1/ IPS8200HQ/ IPS8200HQ-1
Microcontrollers	STM32F401RE/ STM32G431RB
Applications	Programmable Logic Controllers

The **X-CUBE-IPS** expansion software package for **STM32Cube** runs on the STM32 and includes drivers that control the industrial intelligent power switches (IPS) mounted on several expansion boards.

This software allows controlling the output channels of the expansion boards when connected to a **NUCLEO-F401RE** or **NUCLEO-G431RB** development board.

It is possible to build systems with multiple boards stacked to evaluate multichannel digital output modules, even with different output current capabilities.

Each output channel can be switched into the steady-state mode or PWM mode.

In the PWM mode, the software allows you to program the expansion boards to be switched on and off using a specific frequency and duty cycle.

The software included in the package can be used in three integrated development environments (IDEs): IAR, Keil®, and **STM32CubeIDE**.

The expansion boards supported are the following:

- X-NUCLEO-OUT01A2, 8-ch galvanic isolated high-side digital output based on ISO8200BQ
- X-NUCLEO-OUT03A1, 2-ch high-side digital output based on IPS2050H
- X-NUCLEO-OUT04A1, 2-ch high-side digital output based on IPS2050H-32
- X-NUCLEO-OUT05A1, 1-ch high-side digital output based on IPS1025H
- X-NUCLEO-OUT06A1, 1-ch high-side digital output based on IPS1025H-32
- X-NUCLEO-OUT07A1, 4-ch low-side digital output based on IPS4260L
- X-NUCLEO-OUT08A1, 1-ch high-side digital output based on IPS160HF
- X-NUCLEO-OUT09A1, 8-ch high-side digital output based on IPS8160HQ
- X-NUCLEO-OUT10A1, 1-ch high-side digital output based on IPS161HF
- X-NUCLEO-OUT11A1, 8-ch galvanic isolated high-side digital output based on ISO808
- X-NUCLEO-OUT12A1, 8-ch galvanic isolated high-side digital output with SPI interface based on ISO808A
- X-NUCLEO-OUT13A1, 8-ch galvanic isolated high-side digital output based on ISO808-1
- X-NUCLEO-OUT14A1, 8-ch galvanic isolated high-side digital output with SPI interface based on ISO808A-1
- X-NUCLEO-OUT15A1, 1-ch high-side digital output based on IPS1025HF
- X-NUCLEO-OUT16A1, 8-ch high-side digital output with parallel and SPI (8/16-bit) interface based on IPS8200HQ
- X-NUCLEO-OUT17A1, 8-ch high-side digital output with parallel and SPI (8/16-bit) interface based on IPS8200HQ-1
- X-NUCLEO-OUT19A1, 8-ch high-side digital output based on IPS8160HQ-1

1 Detailed description

1.1 What is STM32Cube?

STM32Cube is a combination of a full set of PC software tools and embedded software blocks running on STM32 microcontrollers and microprocessors:

- **STM32CubeMX** configuration tool for any STM32 device; it generates initialization C code for Cortex-M cores and the Linux device tree source for Cortex-A cores
- **STM32CubeIDE** integrated development environment based on open-source solutions like Eclipse or the GNU C/C++ toolchain, including compilation reporting features and advanced debug features
- **STM32CubeProgrammer** programming tool that provides an easy-to-use and efficient environment for reading, writing and verifying devices and external memories via a wide variety of available communication media (JTAG, SWD, UART, USB DFU, I2C, SPI, CAN, etc.)
- **STM32CubeMonitor** family of tools (**STM32CubeMonRF**, **STM32CubeMonUCPD**, **STM32CubeMonPwr**) to help developers customize their applications in real-time
- **STM32Cube MCU and MPU packages** specific to each STM32 series with drivers (HAL, low-layer, etc.), middleware, and lots of example code used in a wide variety of real-world use cases
- **STM32Cube expansion packages** for application-oriented solutions.

1.2 How does this software complement STM32Cube?

This software supports single and multichannel digital output applications.

The package is based on the **STM32CubeHAL**, which is the hardware abstraction layer for the STM32 microcontroller.

The package extends **STM32Cube** by providing a board support package (BSP) for the **STM32 Nucleo** expansion board based on the industrial IPS.

The drivers abstract low-level details of the hardware to access the IPS device data in a hardware-independent manner.

The software package includes a set of examples that the developer can use to start experimenting with the code.

The IPS output channels are controlled via the GPIO peripheral. The application debugging is supported on the respective expansion boards through LEDs, GPIO, and interrupt signals for activity and diagnostics.

Revision history

Table 1. Document revision history

Date	Revision	Changes
09-Jun-2022	1	Initial release.
21-Dec-2022	2	Added references to X-NUCLEO-OUT08A1, X-NUCLEO-OUT09A1, X-NUCLEO-OUT10A1, X-NUCLEO-OUT11A1, X-NUCLEO-OUT12A1, X-NUCLEO-OUT13A1, X-NUCLEO-OUT14A1, X-NUCLEO-OUT19A1, IPS160HF, IPS161HF, ISO808, ISO808-1, IPS8160HQ, IPS8160HQ-1, ISO808A and ISO808A-1.
11-Jul-2023	3	Updated cover image. Added references to X-NUCLEO-OUT01A2 and ISO8200BQ.
06-Oct-2023	4	Updated cover image. Added references to X-NUCLEO-OUT16A1, X-NUCLEO-OUT17A1, IPS8200HQ and IPS8200HQ-1.
20-Dec-2023	5	Updated cover image. Added references to X-NUCLEO-OUT07A1 and IPS4260L.
24-Jul-2024	6	Added reference to STM32CubeMX.

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